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| L Number | Hits | Search Text | DB | Time stamp |
|----------|-------|--|---|------------------|
| 1 | 7523 | (714/25,30,43,44,724,728,733-735,738,739,742).ccls. | USPAT; | 2004/07/22 16:41 |
| | | | US-PGPUB; EPO; JPO; DERWENT; IBM TDB | |
| 2 | 5 | ((714/25,30,43,44,724,728,733-735,738,739,742).ccls.) and (((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) not (((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self*4545)) and (searce \$20 circuit)) or (interface near4 (circuit device))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/22 16:48 |
| - | 370 | "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) interface and (Britt Harris Dooley).XA. | USPAT; US-PGPUB; EPO; JPO; | 2004/07/20 15:18 |
| - | 44538 | 714/\$.ccls. | DERWENT; IBM_TDB USPAT; US-PGPUB; | 2004/07/20 15:18 |
| - | 70 | (interface and (Britt Harris Dooley).XA.) and 714/\$.ccls. | EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; | 2004/07/20 15:18 |
| - | 94 | (interface near4 (circuit device)) and (Britt Harris Dooley).XA. | EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; | 2004/07/20 15:19 |
| - | 32 | ((interface near4 (circuit device)) and (Britt Harris Dooley).XA.) | EPO; JPO; DERWENT; IBM_TDB USPAT; | 2004/07/20 15:58 |
| _ | 435 | and 714/\$.ccls. (((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) | US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; | 2004/07/20 16:24 |
| _ | 400 | or (interface near4 (circuit device))) same (self-test "self test" self\$1test) | US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/07/20 16.24 |
| - | 1 | (((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) and (\$6random near6 generat\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/20 16:06 |
| - | 42 | ((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/20 16:25 |
| - | 626 | (((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/20 16:24 |
| - | 51 | ((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/07/20 16:30 |

| - | 0 | (((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test | USPAT; US-PGPUB; | 2004/07/20 16:30 |
|---|----|--|------------------------|------------------|
| | | "self test" self\$1test)) and (compar\$3 near4 (prescribed | EPO; JPO; | |
| | | expect\$2) near4 (signature data signal))) not | DERWENT: | · |
| | | (((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device | IBM TDB | |
| | | circuit)) or (interface near4 (circuit device))) same (self-test | <u>_</u> | |
| | | "self test" self\$1test)) and (compar\$3 near4 (prescribed | | |
| | | expect\$2) near4 (signature data signal))) | | |
| - | 0 | (((((bidirectional\$2 bi-directional\$2) near4 (buffer device | USPAT; | 2004/07/20 16:30 |
| | | circuit)) or (interface near4 (circuit device))) same (self-test | US-PGPUB; | |
| | ļ | "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) not | EPO; JPO; DERWENT; | |
| | | (((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device | IBM TDB | |
| | | circuit)) or (interface near4 (circuit device))) same (self-test | | |
| | | "self test" self\$1test)) and (compar\$3 near4 (prescribed | | |
| | _ | expect\$2) near4 (signature data signal))) | | |
| - | 9 | (((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device | USPAT; | 2004/07/22 16:47 |
| | | circuit)) or (interface near4 (circuit device))) same (self-test | US-PGPUB; | |
| | | "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) not | EPO; JPO; DERWENT; | |
| | | (((((bidirectional\$2 bi-directional\$2) near4 (buffer device | IBM_TDB | |
| | | circuit)) or (interface near4 (circuit device))) same (self-test | .500 | |
| | | "self test" self\$1test)) and (compar\$3 near4 (prescribed | | |
| | | expect\$2) near4 (signature data signal))) | | |
| - | 84 | (((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device | USPAT; | 2004/07/21 13:46 |
| : | | circuit)) same (interface near4 (circuit device)) same (self-test "self test" self\$1test)) | US-PGPUB; EPO; JPO; | |
| | | Sell test Selly (test)) | DERWENT; | |
| | | , | IBM_TDB | |
| - | 1 | | USPAT; | 2004/07/21 10:08 |
| | | | US-PGPUB | |
| - | 1 | | USPAT; | 2004/07/21 10:08 |
| _ | 1 | | US-PGPUB USPAT; | 2004/07/21 10:09 |
| | ' | | US-PGPUB | 2004/07/21 10.09 |
| - | 1 | | USPAT; | 2004/07/21 10:10 |
| | | | US-PGPUB | |
| - | 1 | | USPAT; | 2004/07/21 10:12 |
| _ | 1 | | US-PGPUB USPAT; | 2004/07/21 10:12 |
| | • | | US-PGPUB | 2004/07/21 10.12 |
| - | 1 | | USPAT; | 2004/07/21 10:12 |
| | | | US-PGPUB | |
| - | 1 | | USPAT; | 2004/07/21 10:12 |
| | 1 | | US-PGPUB | 0004/07/04 40:40 |
| - | • | | USPAT; US-PGPUB | 2004/07/21 10:13 |
| - | 1 | | USPAT; | 2004/07/21 10:13 |
| | | | US-PGPUB | |
| - | 1 | | USPAT; | 2004/07/21 10:14 |
| | 4 | | US-PGPUB | 0004/07/04 40 44 |
| _ | 1 | | USPAT; US-PGPUB | 2004/07/21 10:14 |
| _ | 1 | | USPAT; | 2004/07/21 10:15 |
| | | | US-PGPUB | 2001/07/21 10:10 |
| - | 1 | | USPAT; | 2004/07/21 10:15 |
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| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:15 |
| _ | 1 | | USPAT; | 2004/07/21 10:15 |
| | ' | | US-PGPUB | ~007,017£1 10.10 |
| - | 1 | | USPAT; | 2004/07/21 10:15 |
| | | | US-PGPUB | |
| - | 1 | | USPAT; | 2004/07/21 10:25 |
| _ | 1 | | US-PGPUB USPAT; | 2004/07/24 40:20 |
| - | " | | US-PGPUB | 2004/07/21 10:26 |
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| _ | 1 | | USPAT; US-PGPUB | 2004/07/21 10:26 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:27 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:27 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:28 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:28 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:28 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:28 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:39 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/21 10:39 |
| - | 1 | | USPAT; | 2004/07/21 10:39 |
| - | 1 | | US-PGPUB USPAT; | 2004/07/21 10:40 |
| - | 12 | (US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or | US-PGPUB USPAT; | 2004/07/21 13:44 |
| | | US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did. | US-PGPUB | |
| - | 1 | ((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or | USPAT; | 2004/07/21 13:45 |
| | | US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or | US-PGPUB; EPO; JPO; DERWENT; | |
| - | 1 | US-20030101376-\$).did.) and MISR ((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or | IBM_TDB USPAT; | 2004/07/21 13:47 |
| | | US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.) and ("multiple input shift register" | US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | |
| - | 1 | MISR) (((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) same (interface near4 (circuit device)) same (self-test | USPAT; US-PGPUB; | 2004/07/21 16:05 |
| | | "self test" self\$1test)) and ("multiple input shift register" MISR) | EPO; JPO; DERWENT; IBM TDB | |
| - | 3 | ((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or | USPAT; US-PGPUB; | 2004/07/21 13:55 |
| | | US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.) and ("multiple input shift register" | EPO; JPO; DERWENT; IBM_TDB | · |
| - | 4 | MISR signature) ((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or | USPAT; | 2004/07/21 13:56 |
| | | US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or | US-PGPUB; EPO; JPO; DERWENT; | |
| | | US-20030101376-\$).did.) and ("multiple input shift register" MISR signature compact\$4) | IBM_TDB | |
| - | 1 | (bidirectional\$2 bi-directional\$2) same (interface near4 (circuit device)) same (self-test "self test" self\$1test) and (LFSR) | USPAT; US-PGPUB; EPO; JPO; | 2004/07/21 14:09 |
| | | | DERWENT; IBM_TDB | |
| - | 13 | (interface near4 (circuit device)) same (self-test "self test" self\$1test) and (LFSR) | USPAT; US-PGPUB; EPO; JPO; | 2004/07/21 14:11 |
| | | | DERWENT; IBM_TDB | |

| - | 31 | (interface near4 (circuit device)) same (self-test "self test" self\$1test) and ("linear feedback shift register" LFSR) | USPAT; US-PGPUB; | 2004/07/21 14:11 |
|---|----------|--|------------------------|------------------|
| | | | EPO; JPO; DERWENT; | |
| | | | IBM_TDB | |
| _ | 26 | (interface near4 (circuit device)) same (self-test "self test" | USPAT; | 2004/07/21 14:25 |
| | | self\$1test) and ("linear feedback shift register" LFSR) and | US-PGPUB; | |
| | | ("multiple input shift register" MISR signature) | EPO; JPO; | |
| | | | DERWENT; | |
| _ | 596 | ("low voltage differential signals" LVDS "double data rate" | IBM_TDB USPAT; | 2004/07/22 13:48 |
| | | DDR) near4 interface | US-PGPUB; | 2004/01/22 13.40 |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| _ | 3 | (("low voltage differential signals" LVDS "double data rate" | IBM_TDB USPAT; | 2004/07/21 14:28 |
| - | | DDR) near4 interface) and (self-test "self test" self\$1test) and | US-PGPUB; | 2004/07/21 14:26 |
| | | ("linear feedback shift register" LFSR (pattern near2 | EPO; JPO; | |
| | | generat\$4)) and ("multiple input shift register" MISR signature) | DERWENT; | |
| | | | IBM_TDB | |
| - | 13 | (("low voltage differential signals" LVDS "double data rate" DDR) near4 interface) and (self-test "self test" self\$1test) and | USPAT; | 2004/07/21 14:28 |
| | | ("linear feedback shift register" LFSR (pattern near2 | US-PGPUB; EPO; JPO; | |
| | | generat\$4)"pseudo random generator") | DERWENT; | |
| | | | IBM_TDB | |
| - | 3 | (("low voltage differential signals" LVDS "double data rate" | USPAT; | 2004/07/21 16:08 |
| | | DDR) near4 interface) and (self-test "self test" self\$1test) and ("linear feedback shift register" LFSR (pattern near2 | US-PGPUB; EPO; JPO; | |
| | | generat\$4)"pseudo random generator") and ("multiple input | DERWENT; | |
| | | shift register" MISR signature) | IBM_TDB | |
| - | 2 | (DE-19832307\$ DE-19901460\$).did. | USPAT; | 2004/07/21 14:59 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; DERWENT; | |
| | | | IBM_TDB | |
| - | 62 | (I/O near4 interface) same self-test\$7 | USPAT; | 2004/07/21 16:06 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | İ | | DERWENT; IBM TDB | |
| - | 0 | ((I/O near4 interface) same self-test\$7) and ("linear feedback | USPAT; | 2004/07/21 16:08 |
| | | shift register" LFSR (pattern near2 generat\$4)"pseudo random | US-PGPUB; | |
| | | generator") and ("multiple input shift register" MISR signature) | EPO; JPO; | |
| | | | DERWENT; IBM_TDB | |
| - | 11 | ((I/O near4 interface) same self-test\$7) and ("linear feedback | USPAT; | 2004/07/21 16:11 |
| | | shift register" LFSR (pattern near2 generat\$4)"pseudo random | US-PGPUB; | |
| | | generator") | EPO; JPO; | |
| | | | DERWENT; IBM_TDB | |
| _ | 51 | ((I/O near4 interface) same self-test\$7) not (((I/O near4 | USPAT; | 2004/07/21 16:11 |
| , | | interface) same self-test\$7) and ("linear feedback shift | US-PGPUB; | |
| | | register" LFSR (pattern near2 generat\$4)"pseudo random | EPO; JPO; | |
| | | generator")) | DERWENT; | |
| - | 5 | ("low voltage differential signals" LVDS "double data rate" | IBM_TDB USPAT; | 2004/07/22 10:47 |
| | | DDR) near4 self-test\$7 and (volt\$3 power) | US-PGPUB; | 2004/01/22 10.4/ |
| | | , | EPO; JPO; | |
| | | | DERWENT; | |
| | 2224 | (congrate independent isolated) near4 ((august near4 ::=\tau==) | IBM_TDB | 2004/07/00 40:00 |
| - | 2221 | (separate independent isolated) near4 ((supply near4 voltage) or ((power voltage) near4 supply)) same (buffer I/O interface | USPAT; US-PGPUB; | 2004/07/22 13:03 |
| | | VDD\$4) | EPO; JPO; | |
| | | | DERWENT; | |
| | <u> </u> | | IBM_TDB | |

| - | 1 | ((separate independent isolated) near4 ((supply near4 voltage) or ((power voltage) near4 supply)) same (buffer I/O interface | USPAT; US-PGPUB; | 2004/07/22 10:48 |
|---|------|--|------------------------|------------------|
| | | VDD\$4)) and ("low voltage differential signals" LVDS "double | EPO; JPO; | |
| | | data rate" DDR) same self-test\$7 | DERWENT; | |
| | 50 | | IBM_TDB | 2004/07/00 40 40 |
| - | 50 | ((separate independent isolated) near4 ((supply near4 voltage) or ((power voltage) near4 supply)) same (buffer I/O interface | USPAT; US-PGPUB; | 2004/07/22 10:49 |
| | | VDD\$4)) and self-test\$7 | EPO; JPO; | |
| | | , | DERWENT; | |
| | _ | | IBM_TDB | |
| - | 1167 | ((separate independent isolated) near4 ((power voltage) near4 | USPAT; | 2004/07/22 14:07 |
| | · | supply)) same (buffer I/O VDD\$4) | US-PGPUB; EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 3 | (((separate independent isolated) near4 ((power voltage) | USPAT; | 2004/07/22 13:16 |
| | | near4 supply)) same (buffer I/O VDD\$4)) and DUT near4 board | US-PGPUB; EPO; JPO; | |
| 1 | | board | DERWENT; | ' |
| | | | IBM_TDB | |
| - | 1 | | USPAT; | 2004/07/22 13:14 |
| | | | US-PGPUB | 2004/07/02 42-44 |
| - | 1 | | USPAT; US-PGPUB | 2004/07/22 13:14 |
| _ | 1 | | USPAT; | 2004/07/22 13:14 |
| | | | US-PGPUB | |
| - | 5 | (((separate independent isolated) near4 ((power voltage) | USPAT; | 2004/07/22 13:23 |
| | | near4 supply)) same (buffer I/O VDD\$4)) and ((DUT load) | US-PGPUB; | |
| | | near4 board) | EPO; JPO; DERWENT; | |
| | | | IBM_TDB | |
| - | 90 | (((separate independent isolated) near4 ((power voltage) | USPAT; | 2004/07/22 13:59 |
| | | near4 supply)) same (buffer I/O VDD\$4)) and test\$3 near4 (IC | US-PGPUB; | |
| | | "integrated circuit" semiconductor) | EPO; JPO; DERWENT; | |
| | , | | IBM_TDB | |
| - | 106 | ("low voltage differential signals" LVDS) same PLL | USPAT; | 2004/07/22 13:49 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; DERWENT; | |
| | | | IBM TDB | |
| - | 2 | (("low voltage differential signals" LVDS) same PLL) and LFSR | USPAT; | 2004/07/22 13:49 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; IBM TDB | |
| - | 133 | (((separate independent isolated) near4 ((power voltage) | USPAT; | 2004/07/22 13:59 |
| | | near4 supply)) same (buffer I/O VDD\$4)) and interference | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; IBM_TDB | |
| - | 44 | (((separate independent isolated) near4 ((power voltage) | USPAT; | 2004/07/22 14:07 |
| | | near4 supply)) same (buffer I/O VDD\$4)) and interference | US-PGPUB; | |
| | | same supply | EPO; JPO; | |
| | 1 | | DERWENT; IBM_TDB | |
| _ | 147 | ((power voltage) near4 supply) same ((buffer I/O VDD\$4) | USPAT; | 2004/07/22 16:40 |
| | | near4 ring) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; IBM TDB | |
| _ | 1 | | USPAT; | 2004/07/22 15:01 |
| | | | US-PGPUB | |
| - | 1 | | USPAT; | 2004/07/22 15:02 |
| | 4 | | US-PGPUB USPAT; | 2004/07/22 45:02 |
| _ | 1 | | US-PGPUB | 2004/07/22 15:02 |
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| | | US-PGPUB |